

ABSTRACT OF THE DISCLOSURE

An In-Plane switching mode LCD device includes a plurality of gate and data lines crossing each other to define a plurality of pixel regions, a plurality of thin film transistor (TFT) formed at crossing points of the gate and data lines to be alternately positioned along lower and upper side pixel regions adjacent to corresponding gate lines, a plurality of storage lines disposed in an offset configuration to be parallel with the gate lines along the TFT regions, a plurality of pixel electrodes within the pixel regions to be connected to drain electrodes of the TFTs, and a plurality of common electrodes disposed at fixed intervals from the pixel electrodes to be connected to the storage lines.